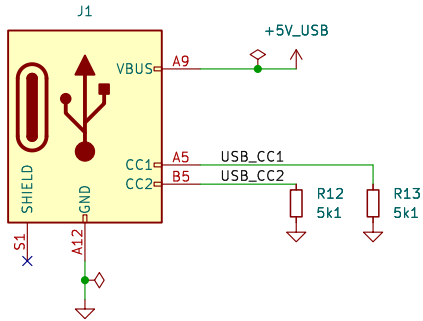
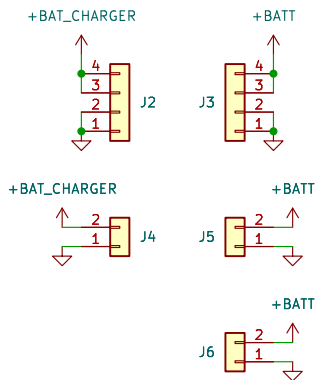


Power Input



Output Connectors



Charger IC

VPCC:
Reduce charging if VPCC < 1.23V
 $V_{MIN} = 1.23V / (R2 / (R1 + R2))$

CE:
H=Charging enabled

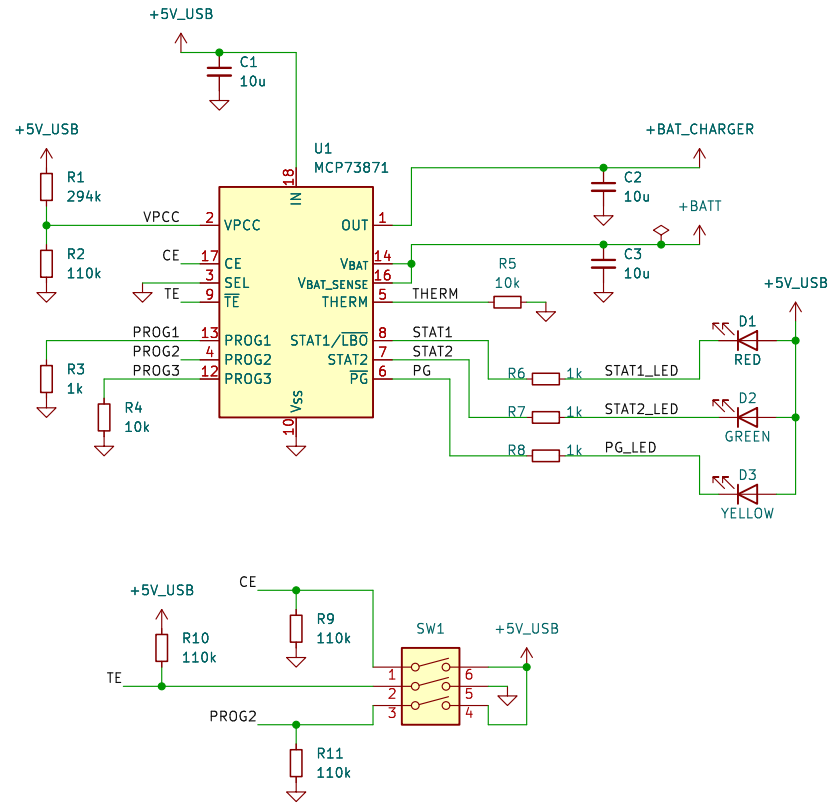
SEL:
L=USB input
H=AC-DC adapter input

TE:
L=Stop charging after 6H

PROG1:
 $I_{REG} = 1000 / R_{PROG1}$
* via USB, 500 mA is max
* precondition is 10% of I_{REG}

PROG2:
L=100mA USB current limit
H=500mA USB current limit

PROG3:
Terminate charging at I_{TERM}
 $I_{TERM} = 1000 / R_{PROG3}$



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Sheet: /

File: batt_charger.kicad_sch

Title: BATT-CHARGER

Size: A4 Date: 2023-09-19

KiCad E.D.A. kicad 7.0.7

Rev: 2

Id: 1/1