

The schematic shows the USB connector J1 with the following connections:

- A4** (VBUS) connects to **PWR_FLAG**, which is tied to **+5V**.
- A5** (USB_CC1) and **B5** (USB_CC2) are connected to pull-up resistors **R1** and **R2** (both 5k1) to **+5V**.
- A7** (USB_D-) and **B7** (USB_D+) are connected to the **USB_D-** signal.
- A6** (USB_D+) and **B6** (USB_D-) are connected to the **USB_D+** signal.
- A8** (SBU1) and **B8** (SBU2) are connected to the shield (S1).
- A1** (GND) is connected to the shield (S1).
- PWR_FLAG** is also connected to **GND**.

U1
ESP32-S3-WROOM-1

3V3 2

EN 3

5k1 R5

100n C5

SW1

+3V3

100n C3

22u C4

+3V3

100 BOOT

101

102

103 JTAG_SELECT

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J2

I015_1	2_EN
I016_3	4_I04
I017_5	6_I05
I018_7	8_I06
I046_BOOT_MSG_9	10_I07
I03_JTAG_SELECT_11	12_I08
I010_13	14_I09
I019_USB_D-_15	16
I020_USB_D+_17	18
19	20

J3

I01_1	2_I038
I02_3	4_I045_SPI_VDD_LVL
I044_RXD0_5	6_I048_1V8
I043_TXD0_7	8_I047_1V8
I042_TMS_9	10_I021
I041_TDI_11	12_I014
I040_TDO_13	14_I013
I039_TCK_15	16_I012
I00_BOOT_17	18_I011
19	20

Power connections: +5V to J2_18 and J3_17; +3V3 to J2_16 and J3_14.

The diagram shows four nodes arranged horizontally, separated by vertical dashed lines. Each node consists of a red circle with a white center. To the right of each circle are the labels 'H1 M2', 'H2 M2', 'H3 M2', and 'H4 M2' respectively. Below the first two nodes (H1 and H2) is a ground symbol, represented by a downward-pointing triangle with a horizontal line at its base. The third and fourth nodes (H3 and H4) do not have a ground symbol.

Id: 1/1